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FIG. 2(a) Current art SRAM operation

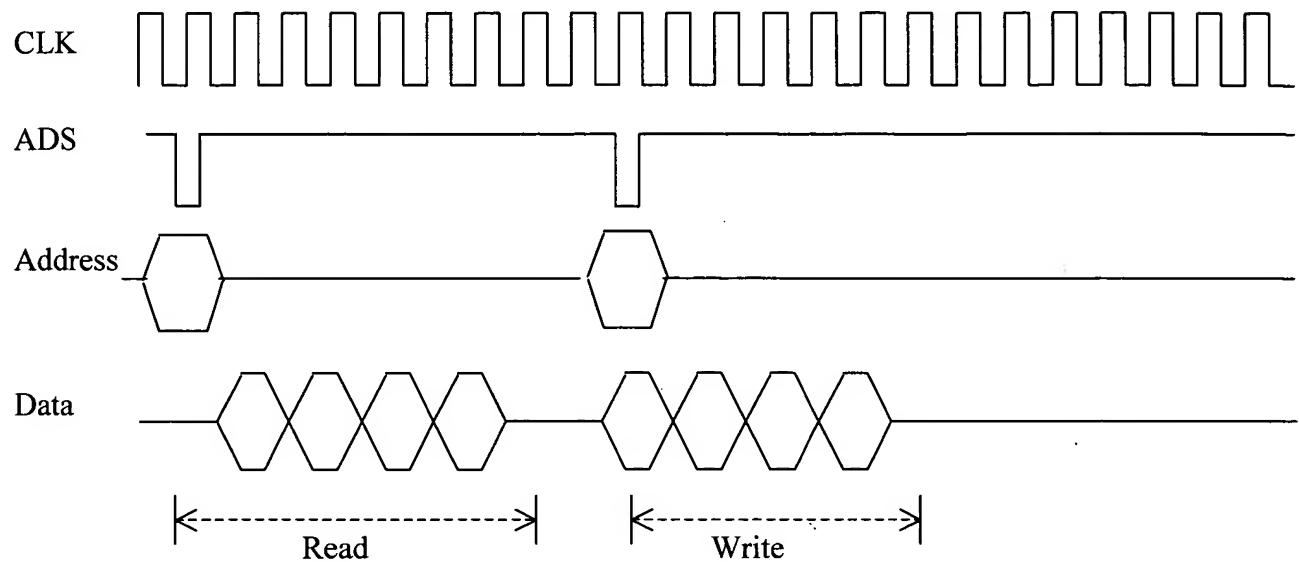


FIG. 2(b) Current art DRAM operation

